

Student: _____

Date: _____

2023 - 2024

netID: _____

Advisor: _____

Year 1

Semester 1	Cr	Status	Semester 2	Cr	Status
MA: MATH 1234 - Calculus I*	4		MA: MATH 1248 - Calculus II* <i>MATH 1234</i>	4	
QD: CS 1210 - Computer Programming I	3		N2, QD: PHYS 1500 - Physics for Engineers I <i>MATH 1234</i>	4	
N2, QD: CHEM 1400 - General Chemistry 1	4		PHYS 1510 - Physics Problem Solving I [Optional]	[1]	
Catamount Core (WIL1): HCOL 1000 - FY Writing Seminar	3		EE 1100 - EE Principles & Design	2	
CEMS 1500 - CEMS First Year Seminar	1		MATH 2500 - Eng Math Linear Algebra Lab <i>MATH 1234; Coreq: MATH 1248</i>	1	
ENGR 1020 - Graphical Communication [Optional]	[2]		HCOL 1500 - FY Research Presentation Seminar	3	
			Catamount Core	3	
<i>Total credits</i>	15-17		<i>Total credits</i>	17-18	

Year 2

Semester 1	Cr	Status	Semester 2	Cr	Status
EE 2125 - Circuits I <i>MATH 1248</i>	4		EE 2135 - Circuits II <i>EE 2125</i>	4	
MA: MATH 2248 - Calculus III <i>MATH 1248</i>	4		EE 2185 - Circuits Design Project <i>EE 2125</i>	2	
N1, QD: PHYS 1550 - Physics for Engineers II <i>PHYS 1500; MATH 1248; Coreq: MATH 2248</i>	3		MATH 3201 - Adv Engineering Mathematics <i>MATH 2248</i>	3	
PHYS 1560 - Physics Problem Solving II [Optional]	[1]		EE 2810 - Fundamentals of Digital Design <i>CS 1210</i>	4	
CEE 1150 - Applied Mechanics <i>MATH 1248; PHYS 1500</i>	3		STAT 2510 - Applied Probability <i>MATH 1248</i>	3	
HCOL 2000 - Sophomore Seminar	3		HCOL 2000 - Sophomore Seminar	3	
<i>Total credits</i>	17-18		<i>Total credits</i>	19	

Year 3

Semester 1	Cr	Status	Semester 2	Cr	Status
EE 3110 - Electronics I <i>PHYS 1550; EE 2135</i>	4		WIL2: EE 3415 - Electronics Design Project <i>EE 3115; EE 3110; WIL1</i>	3	
EE 3115 - Electronics Laboratory <i>Pre/Coreq: EE 3110</i>	2		Junior EE Elective	4	
EE 3150 - Signals & Systems <i>MATH 3201; Pre/Coreq: EE 2135</i>	4		Junior EE Elective	4	
Junior EE Elective	4		EE 3000 - Engineering Ethics/Leadership	1	
Free Elective (2000 Level or Higher)	3		Catamount Core	3	
CEMS 2010 - HCOL Research Experience	1		CEMS 2020 - Research Thesis Proposal	1	
<i>Total credits</i>	18		<i>Total credits</i>	16	

Year 4

Semester 1	Cr	Status	Semester 2	Cr	Status
EE 4100 - Capstone Design I <i>EE 3110 or EE 3150; EE 3415</i>	3		EE 4200 - Capstone Design II <i>EE 4100</i>	3	
EE 3100 - Electromagnetic Field Theory <i>PHYS 1550; MATH 2248; EE 2135</i>	4		EE 2996 - College Honors (Honors Thesis)	3	
SU: EMGT 2041 - Engineering Economics <i>MATH 1248</i>	3		Free Elective (2000 Level or Higher)	3	
EE 2996 - College Honors (Honors Thesis)	3		Catamount Core	3	
Catamount Core	3		Catamount Core	3	
<i>Total credits</i>	16		<i>Total credits</i>	15	

Minimum Total Credits Required for Degree: 128

This document is an advising tool and should be used in combination with a student's degree audit, as well as the published Catalogue for 2023-2024 found at <http://catalogue.uvm.edu/>

Prerequisite courses are listed below the course name in italics. Prerequisites listed are only for courses, as relevant to your specific degree program, and may have other registration restrictions. Please refer to the catalogue.

* Grade of C- or higher required

Junior EE Elective: Please refer to your degree audit to see course options.

Free Elective (2000 Level or Higher): Free Electives allow students to further tailor their studies through, e.g., technical, general, and/or professional development electives. Students are encouraged to work with their advisor(s) to select courses that complement their curricula and support their academic and career goals (such as the pursuit of an EE certificate, additional EE courses in their field of interest, a minor in another discipline, or semester abroad).

Catamount Core: Students may take courses that fulfill more than one Catamount Core requirement, but they must still take at least 40 unique credits of courses that have been approved to fulfill Catamount Core requirements.

Students are encouraged to overlap Catamount Core requirements with their PLHC required courses (HCOL 1500 and both HCOL 2000 courses)